

WEST Search History

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DATE: Tuesday, December 30, 2003

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		<i>DB=JPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L5	mutual conductance and read adj2 transistor	1
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L4	L3 and read adj2 transistor	15
<input type="checkbox"/>	L3	L2 and threshold	51
<input type="checkbox"/>	L2	L1 and mutual conductance	58
<input type="checkbox"/>	L1	memory and floating gate	12639

END OF SEARCH HISTORY

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